

Education

Harvard University Ph.D., Electrical and Computer Engineering • Advisors: Prof. Vijay Janapa Reddi, Prof. Yilun Du	Cambridge, MA Fall 2026 - Expected 2030
Princeton University M.S.E., Electrical and Computer Engineering • Master Thesis: Optimizing Efficiency of Cognitive Agents with Improved Performance (Advisor: Prof. Tom Griffiths)	Princeton, NJ Sep. 2023 - May 2025
Tsinghua University B.E., Electronic Engineering GPA: 3.93/4.0, Ranking: 11/260+ • Outstanding Graduate (Top 5%), Outstanding Bachelor Thesis Award	Beijing, China Aug. 2019 - Jun. 2023

Work Experience

Harvard University Research Assistant	Cambridge, MA Jun. 2025 - Present
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Publications

* indicates equal contribution.

LLM and AI

- Zhenting Qi, Huangyuan Su, Ao Qu, **Chenyu Wang**, Yu Yao, Han Zheng, Kushal Chattopadhyay, Guowei Xu, Zihan Wang, Weirui Ye, Vijay Janapa Reddi, Ju Li, Paul Pu Liang, Himabindu Lakkaraju, Sham Kakade, Yilun Du. [“Economy of Minds: Emerging Multi-Agent Intelligence with Economic Interactions,”](#) preprint, 2026.
- Chenyu Wang**^{*}, Zishen Wan^{*}, Hao Kang, Emma Chen, Zhiqiang Xie, Tushar Krishna, Vijay Janapa Reddi, Yilun Du. [“SLM-MUX: Orchestrating Small Language Models for Reasoning,”](#) accepted by *International Conference on Learning Representations (ICLR)*, 2026.
- Chenyu Wang**, Yihan Wang. [“Evaluating Zero-Shot Long-Context LLM Compression,”](#) preprint, 2024.
- Yi Cai^{*}, **Chenyu Wang**^{*}, Zixuan Zhou, Xuefei Ning, Huazhong Yang, Yu Wang. [“DeepGuise: Learning to Disguise Neural Architectures for Impeding Adversarial Transfer Attacks,”](#) preprint, 2023.

AI for Systems

- Chenyu Wang**^{*}, Zishen Wan^{*}, Jeffrey Ma, Shvetank Prakash, Zhenting Qi, Haebin Do, Andy Cheng, Arya Tschand, Jiahe Shi, Yilun Du, Vijay Janapa Reddi. [“ArchEval: Measuring AI Agents as Computer Architects,”](#) presented at the *Architecture 2.0 Workshop at ISCA*, 2026.
- Shvetank Prakash, Andrew Cheng, Arya Tschand, Mark Mazumder, et al. (including **Chenyu Wang**). [“QUARCH: A Benchmark for Evaluating LLM Reasoning in Computer Architecture,”](#) accepted by *International Conference on Machine Learning (ICML)*, 2026.
- Chenyu Wang**^{*}, Jiahe Caroline Shi^{*}, David Kong, Duane S. Boning, Zishen Wan, Yilun Du, Vijay Janapa Reddi. [“AgentDSE: Reasoning-Augmented Architectural Design Space Exploration,”](#) accepted by *ISCA 2026 Workshop on Machine Learning for Architecture and Systems (MLArchSys)*, 2026.
- Hanbo Sun, Zhenhua Zhu, **Chenyu Wang**, Xuefei Ning, Guohao Dai, Huazhong Yang, Yu Wang. [“Gibbon: Efficient Co-Exploration of NN Model and Processing-in-Memory Architecture,”](#) *IEEE Transactions on Computer-Aided Design (TCAD)*, 2023.
- Hanbo Sun^{*}, **Chenyu Wang**^{*}, Zhenhua Zhu, Xuefei Ning, Guohao Dai, Huazhong Yang, Yu Wang. [“Gibbon: Efficient Co-Exploration of NN Model and PIM Architecture,”](#) accepted by *Design, Automation and Test in Europe Conference (DATE)*, 2022.

AI Accelerator & Infrastructure Design

- Zishen Wan, Hanchen Yang, Jiayi Qian, Ritik Raj, Joongun Park, **Chenyu Wang**, Arijit Raychowdhury, Tushar Krishna. [“Compositional AI Beyond LLMs: System Implications of Neuro-Symbolic-Probabilistic Architectures,”](#) accepted by *Architectural Support for Programming Languages and Operating Systems (ASPLOS)*, 2026.
- Chenyu Wang**^{*}, Zhen Dong^{*}, Daquan Zhou^{*}, Zhenhua Zhu, Yu Wang, Jiashi Feng, Kurt Keutzer. [“Efficient Processing-in-Memory Accelerators based on Epite,”](#) accepted by *Design Automation Conference (DAC)*, 2024.

Professional Service

Reviewer: NeurIPS, ICML, COLM (Conference on Language Modeling).

Honors & Awards

2023–2024	Gordon Wu Fellowship (Princeton University)
2023	Outstanding Graduate of Tsinghua University & Beijing
2020	First Prize, Chinese Undergraduate Physics Competition
2018	Silver Medal, Chinese Physics Olympiad